

## WEST Search History

DATE: Saturday, January 08, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=USPT; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L3	6460172.pn. and ((automatic\$ or dynamic\$) near5 configur\$)	1
<input type="checkbox"/>	L2	6460172.pn. and ((memory or ram) near5 configur\$)	1
<input type="checkbox"/>	L1	6460172.pn. and configur\$	1

END OF SEARCH HISTORY



## Refine Search

### Search Results -

Term	Documents
ARRAY	677734
ARRAYS	191528
MATRIX	608515
MATRICES	78108
MATRIXES	9024
MATIRCES	4
MATIRCE	0
(19 AND (MATIRCES OR MATRIX OR ARRAY)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	32
(L19 AND (ARRAY OR MATRIX OR MATIRCES)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	32

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L24

Refine Search

Recall Text

Clear

Interrupt

### Search History

**Set**  
**Name**  
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**Query**

**Hit**  
**Count**

**Set**  
**Name**  
 result  
 set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L24 L19 and (array or matrix or matirces)

32 L24

L23 L20 and (array or matrix or matirces)

122 L23



<u>L22</u>	L21 and (array or matrix or matirces)	99	<u>L22</u>
<u>L21</u>	L10 and l16	111	<u>L21</u>
<u>L20</u>	L9 and l16	195	<u>L20</u>
<u>L19</u>	L17 and l16	59	<u>L19</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L18</u>	L17 and l16	59	<u>L18</u>
<u>L17</u>	(714/37-46)![CCLS]	3430	<u>L17</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L16</u>	L15 and (eeprom or erasable)	8678	<u>L16</u>
<u>L15</u>	microcontroller\$1 and program\$8 (cell\$1 or block\$1) and (macro or macro near1 cell\$1) and (analog or analogue or digital)	42877	<u>L15</u>
<u>L14</u>	l11 and (macro or macro near1 cell\$1)	2	<u>L14</u>
<u>L13</u>	L12 and macro\$1	2	<u>L13</u>
<u>L12</u>	l8 and l10	12	<u>L12</u>
<u>L11</u>	l8 and l9	8	<u>L11</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L10</u>	(716/1-21)![CCLS]	7141	<u>L10</u>
<u>L9</u>	(712/2-300)[CCLS]	10267	<u>L9</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L8</u>	microcontroller\$1 and program\$8 near8 (cell\$1 or block\$1) near15 (analog or analogue or digital)	302	<u>L8</u>
<u>L7</u>	microcontroller\$1 and program\$8 near8 (cell\$1 or block\$1) near35 (analog or analogue or digital)	316	<u>L7</u>
<u>L6</u>	L5 not l3	1	<u>L6</u>
<u>L5</u>	L4 and (eeprom or erasable)	16	<u>L5</u>
<u>L4</u>	microcontroller\$1 near25 program\$8 near6 (cell\$1 or block\$1) near35 (analog or analogue or digital)	46	<u>L4</u>
<u>L3</u>	L2 and (eeprom or erasable)	15	<u>L3</u>
<u>L2</u>	microcontroller\$1 near25 program\$8 near6 (cell\$1 or block\$1) near12 (analog or analogue or digital)	43	<u>L2</u>
<u>L1</u>	microcontroller\$1 near25 program\$8 near12 (analog or analogue or digital)	2437	<u>L1</u>

END OF SEARCH HISTORY